

ABSTRACT OF THE DISCLOSURE

5 An operational amplifier circuit is constituted by first and second
inverted amplifier circuits (A1, A2) that receive first and second input
signals, a third inverted amplifier circuit (A3) that receives an
estimated common-mode output signal and an output signal from the
first inverted amplifier circuit and outputs first and second output
signals, a fourth inverted amplifier circuit (A4) that receives the
10 estimated common-mode output signal and an output signal from the
second inverted amplifier circuit and outputs third and fourth output
signals, where the estimated common-mode output signal is generated
by adding the second output signal and the fourth output signal, and
first and second non-inverted amplifier circuits (A5, A6) that receive the
15 estimated common-mode output signal and feed it back to the first and
second inverted amplifier circuits.